

Professor Mark Zwolinski

BSc, PhD, CEng, FIET, FBCS, SMIEEE, SMACM

Deputy Head of Electronics and Computer Science
(Education)

Professor of Electronics and Computer Science

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Biography

Mark Zwolinski obtained his PhD in 1986 from the University of Southampton. His thesis was on the topic of circuit simulation. He was appointed a lecturer in ECS in 1990 and was promoted to Senior Lecturer in 1999 and to Reader in 2002. He was awarded a personal chair in 2005.

Prof Zwolinski is a Fellow of the IET and of the BCS and a Chartered Engineer. He is a Senior Member of IEEE and of ACM. He is a member of EPSRC's peer review college. He is a member of the editorial board of Microelectronics and Reliability. Prof Zwolinski was Program Chair of IEEE ICECS in 2007 and is General Co-Chair of VDAT in Jaipur, India, 2013. He has served and is serving on the Technical Program Committees of international conferences including: DAC, DATE, ISCAS and VLSI Design.

Prof Zwolinski has published over 140 refereed papers and has supervised over 20 PhD students to completion. He is co-author of the book "VLSI Circuit Simulation and Optimization" and author of two text books: "Digital System Design with VHDL" and "Digital System Design with SystemVerilog".

Research interests

- Electronic Design Automation
- Circuit Simulation
- Energy and Performance Modelling
- Design for Reliability
- CMOS Design for Manufacture